

IN THE SPECIFICATION:

Please REPLACE the Paragraph 25 with the following paragraph:

[0025] Figure 2 is a functional block diagram relating to the data acquisition and data processing in data acquisition apparatus MC. The data acquisition apparatus MC has input interfaces S1..S4, as has already been explained in principle with reference to Figure 1. The first interface S1 is, for example, in the form of a CAN interface (CAN = Controller Area Network). In the exemplary embodiment illustrated in Figure 3, the second interface S2 is in the form of a serial interface, for example for connection of a keyboard or keypad, while the third interface S3 is in the form of an on-board input/output interface, for example for connection of sensors, encoders etc. The fourth interface S4 is used for connection of the data from a GPS module (GPS = Global Positioning System). The input signals I1..I4 supplied via the interfaces S1..S4 are passed on via an address allocation unit AZ and a data converter EA to a signal processing apparatus 1. The signal processing apparatus 1 has an output interface SA, via which output signals 18 are passed on to a communication driver KT, for example a GSM driver. The signal processing apparatus 16-1 contains means A, L, D, M for recording and monitoring input signals I1..I4, which can be predetermined, at times which can be predetermined. In detail, the means L, A, D, M comprise a data analysis unit D, which is intended for recording selected input signals I1..I4 at times which can be predetermined, with the recording rules for short-term monitoring of information which can be derived from the input signals 1a..4a being defined in advance from the control center. The corresponding rules are stored in a data analysis rule interpreter DR. The rules which are stored in the memory DR can be loaded remotely via the output interface SA, via a link D1. The data signals d determined by means of the data analysis unit D are recorded by means of the recording apparatus DA and a data buffer DP. The data processing unit L is constructed in a similar way. The data processing unit L also contains a memory LR for storage of the rules for recording the input signals 1 to be processed by means of the data processing unit L. A preprocessing unit LV, a data recording unit LA and a data buffer LP are also provided. Further processing units in the signal processing apparatus 16 are the alarm unit A for monitoring information data which can be derived from the input signals 1a..4a in accordance with alarm rules which can be predetermined. The alarm unit A comprises a memory AR for storage of the alarm monitoring rules. The alarms are monitored by evaluation of the alarm signals a supplied to the alarm unit, which are passed on to an alarm monitoring unit AÜ and an alarm archive AA.